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03/15/2006

LSI Logic Corporation
1621 Barber Lane, M/S D-106
Milpitas, CA 95035-7458

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Manu Kashyap	(Depositor's name)
<i>[Signature]</i>	(Signature)
June 7, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/658,168	09/08/2003	John A. Knoch	03-0485 81578	5690

TITLE OF INVENTION: METHOD OF QUALIFYING A PROCESS TOOL WITH WAFER DEFECT MAPS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	06/15/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS			
KHUU, HIEN DIEU THI	2863	702-081000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list

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1 Eric James Whiresell

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

LSI Logic Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Milpitas, California USA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature *[Signature]*Date June 7, 2006Typed or printed name Timothy CrollRegistration No. 36,771

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Docket Number: 03-0485
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